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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/509,338	10/08/2004	Takehiro Ueda	260055US2SPPCT	2024
22850 7590 04/03/2008 OBLON, SPIVAK, MCCLELLAND MAIER & NEUSTADT, P.C. 1940 DUKE STREET ALEXANDRIA, VA 22314			EXAMINER DAHIMENE, MAHMOUD	
			ART UNIT 1792	PAPER NUMBER
			NOTIFICATION DATE 04/03/2008	DELIVERY MODE ELECTRONIC

**Please find below and/or attached an Office communication concerning this application or proceeding.**

The time period for reply, if any, is set in the attached communication.

Notice of the Office communication was sent electronically on above-indicated "Notification Date" to the following e-mail address(es):

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<b>Office Action Summary</b>	<b>Application No.</b> 10/509,338	<b>Applicant(s)</b> UEDA ET AL.	
	<b>Examiner</b> MAHMOUD DAHIMENE	<b>Art Unit</b> 1792	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

### Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

### Status

- 1) ☒ Responsive to communication(s) filed on 07 February 2008.
- 2a) ☐ This action is **FINAL**.                      2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

### Disposition of Claims

- 4) ☒ Claim(s) 2-7,9-18 and 20-26 is/are pending in the application.
- 4a) Of the above claim(s) \_\_\_\_\_ is/are withdrawn from consideration.
- 5) ☐ Claim(s) \_\_\_\_\_ is/are allowed.
- 6) ☒ Claim(s) 2-7,9-18,20-23 and 25 is/are rejected.
- 7) ☒ Claim(s) 24 and 26 is/are objected to.
- 8) ☐ Claim(s) \_\_\_\_\_ are subject to restriction and/or election requirement.

### Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on \_\_\_\_\_ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.  
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).  
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

### Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All    b) ☐ Some \*    c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
  2. ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
  3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

\* See the attached detailed Office action for a list of the certified copies not received.

### Attachment(s)

- |  |   |
|--|---|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892)          | 4) <input type="checkbox"/> Interview Summary (PTO-413)           |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | Paper No(s)/Mail Date. _____                                      |
| 3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO/SB/08)          | 5) <input type="checkbox"/> Notice of Informal Patent Application |
| Paper No(s)/Mail Date _____  | 6) <input type="checkbox"/> Other: _____                          |

## **DETAILED ACTION**

### ***Continued Examination Under 37 CFR 1.114***

1. A request for continued examination under 37 CFR 1.114, including the fee set forth in 37 CFR 1.17(e), was filed in this application after final rejection. Since this application is eligible for continued examination under 37 CFR 1.114, and the fee set forth in 37 CFR 1.17(e) has been timely paid, the finality of the previous Office action has been withdrawn pursuant to 37 CFR 1.114. Applicant's submission filed on 2/7/2008 has been entered.

### ***Claim Objections***

2. Claims 24 and 26 are objected to because of the following informalities: The term "charging" has, potentially, different meanings such as charging with electrostatic charges or simply loading the chuck (with a wafer, for instance). If electrostatic charge buildup is to be suppressed in the context of claims 24 and 26, the term "charging" should be associated with such electrostatic or otherwise electrical charges. Appropriate correction is required.

### ***Claim Rejections - 35 USC § 103***

1. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a

whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

2. This application currently names joint inventors. In considering patentability of the claims under 35 U.S.C. 103(a), the examiner presumes that the subject matter of the various claims was commonly owned at the time any inventions covered therein were made absent any evidence to the contrary. Applicant is advised of the obligation under 37 CFR 1.56 to point out the inventor and invention dates of each claim that was not commonly owned at the time a later invention was made in order for the examiner to consider the applicability of 35 U.S.C. 103(c) and potential 35 U.S.C. 102(e), (f) or (g) prior art under 35 U.S.C. 103(a).

3. Claim s 1, 7, 9-12, 18, 20-23, 25, are rejected under 35 U.S.C. 103(a) as being unpatentable over Flanigan et al. (US 6,081,414) in view of Ohmi et al. (US 6,217,633).

Regarding claims 1, 7, 12, 7, 12, 18, 23, 25, the reference of Flanigan et al. (US 6,081,414) describes an apparatus for improved biasing and retaining a workpiece in a plasma process chamber comprising an electrostatic chuck-pedestal (block) (figure 2) having a flow path of a heat medium in an inner part (236 and 220), a component (105) in the vacuum processing chamber disposed to be in contact with the block and made at least partly of an insulative material. The pedestal temperature is controlled by circulating an insulative fluid (coolant) (column 2, line 44) as the heat medium in the flow path (236). The wafer is transferred to/from the chamber from/to a loadlock (figure 1) the wafer is plasma processed. The coolant is continuously circulating even during wafer transfer when no plasma is generated in order to maintain chuck temperature during wafer transfer. The vacuum in the chamber is controlled for PVD or other

Art Unit: 1792

processes. Flanigan cites “In operation, a wafer (102) is placed on the support surface (103) of the electrostatic chuck (105). Air is drawn out of the chamber (100) via a vacuum pump (128) to create a low pressure environment (i.e. 1 mTorr to 5 Torr). A reactant gas, preferably Argon (argon is an inert gas), is introduced into the chamber (100) from one of the remote gas sources” (column 7, line 19), pressure is controlled during Argon flow.

carrying the object to be processed into the vacuum processing chamber generating plasma to plasma-process the object to be processed and carrying the object to be processed that has undergone the processing out of the vacuum processing chamber between said processing of the object to be processed and processing of a subsequent object to be processed, circulating the insulating fluid in the flow path while the object to be processed is not in the vacuum processing chamber and no plasma is generated are steps conventionally used in semiconductor wafer processing as suggested by Flanigan.

It is noted that Flanigan is silent about controlling pressure in the vacuum processing chamber to a predetermined pressure while supplying inert gas into the vacuum processing chamber.

Ohmi teaches a process for recovering rare gases used in semiconductor processing systems such as PVD. Ohmi discloses “In the magnetron sputtering system, aluminum was used as a film-forming solid material. Meanwhile, the gate valve interposed between the process chamber and the loading chamber was operated only during loading and unloading of wafers, and the wafer loading/unloading time was 30

sec. **Before loading/unloading wafers, nitrogen gas was introduced as a purge gas into the loading chamber and the process chamber** to provide a pressure of 1 Pa.” (column 11, line 61). Nitrogen gas is an inert gas when not excited by a plasma and is conventionally used for purging. It is noted that Ohmi does not expressly disclose the pressure range of not lower than 13 Pa nor higher than about 40 Pa, however, it would appear that purging pressure is an adjustable parameter, it would have been obvious to one of ordinary skill in the art at the time the invention was made to adjust the purging pressure, since it has been held that the provision of adjustability, where needed, involves only routine skill in the art and the determination of workable ranges is not considered inventive.

One of ordinary skill in the art would have been motivated to purge the deposition chamber of Flanigan in a manner as described by Ohmi in order to purge accumulated film forming solid materials deposited on the gate valve area interposed between the process chamber and the loading chamber, material loosely deposited which would be released when the valve is operated only during loading and unloading of wafers.

As to claims 9, 10, 11, 20, 21, 22, in PVD or any other plasma process the pressure in the process chamber is controlled by a computer system which conventionally operates with a predetermined sampling rate for measuring and regulating the pressure which makes the controlling process intermittent. When the control loop is initiated, the pressure is controlled even when flow rates are varied or fixed.

***Claim Rejections - 35 USC § 103***

4. Claims 2, 13 are rejected under 35 U.S.C. 103(a) as being unpatentable over Flanigan et al. (US 6,081,414) in view of Ohmi et al. (US 6,217,633) and further in view of Watanabe et al. (US 5,625,526).

Regarding claims 2, 13, it is noted that Flanigan is silent about a fluorinated refrigerant.

The reference of Watanabe describes an electrostatic chuck system wherein a fluorinated refrigerants such as (R-22) are conventionally used as a cooling medium (column 23, line 36).

Therefore, it would have been obvious to one of ordinary skill in the art at the time the invention was made to modify the process of Flanigan to include a fluorinated refrigerants such as (R-22) for cooling the pedestal because Watanabe teaches those refrigerants are conventionally used for wafer cooling. One of ordinary skill in the art would have been motivated to use a fluorinated refrigerant when processing requires low wafer temperature.

***Claim Rejections - 35 USC § 103***

Claims 3, 4, 5, 14, 15, 16, are rejected under 35 U.S.C. 103(a) as being unpatentable over Flanigan et al. (US 6,081,414) in view of Ohmi et al. (US 6,217,633) and further in view of Nagasaki (US 6,215,643).

Regarding claims 3, 4, 5, 14, 15, 16, it is noted that Flanigan is silent about the volume resistivity of the ceramic.

The reference of Nagasaki describes a ceramic electrostatic chuck, with an aluminum base (11), system and discloses to establish the Johnson-Rahbeck effect, a ceramic portion present between an electrode and an attractive surface of an electrostatic chuck needs to have a volume resistivity of not lower than  $10^9 \Omega\text{-cm}$  and lower than  $10^{11} \Omega\text{-cm}$  in a process temperature range. (column 1, line 45).

Therefore, it would have been obvious to one of ordinary skill in the art at the time the invention was made to modify the process of Flanigan to include ceramic as an insulative material with volume resistivity  $10^9 \Omega\text{-cm}$  or higher positioned on an aluminum base because Nagasaki teaches the resistivity range of  $10^9 \Omega\text{-cm}$  to  $10^{11} \Omega\text{-cm}$  are conventional for electrostatic chucks. Overlapping ranges are held obvious.

### ***Claim Rejections - 35 USC § 103***

5. Claims 6, 17, are rejected under 35 U.S.C. 103(a) as being unpatentable over Flanigan et al. (US 6,081,414), in view of Ohmi et al. (US 6,217,633) and Nagasaki (US 6,215,643) and further in view of Paschen (from F. Paschen published paper (Wied. Ann., 37, 69, 1889) see attached page).



Regarding claims 6, 17, it is noted Flanigan is silent about setting a pressure range for a given gap distance between electrodes in the chamber based on the Paschen curve for a given gas including nitrogen.

In general Paschen curves for different gases indicate voltage breakdown of the gas as a function of pressure (and gap distance) as shown in the attached figure from the reference of Paschen.

Therefore, it would have been obvious to one of ordinary skill in the art at the time the invention was made to modify the method of Flanigan to include the teachings of Paschen which indicate, that for a given gas and gap distance, ignition of a plasma has a minimum voltage, the pressure region around this minimum require a voltage only slightly higher than the minimum breakdown voltage. One of ordinary skill in the art would have been motivated to select a pressure range with a lower limit high enough including 0.6 times the lowest sparking pressure for nitrogen as defined by the proper Paschen curve, and a higher limit not too high such as to create a high pressure back-flow from the process chamber to the transfer chamber in order to prevent ignition when not needed. Applicant has not shown any unexpected results in selecting a pressure not lower than 0.6 times nor higher than 2.0 times the pressure yielding the lowest breakdown voltage (or 13 Pa to 40 Pa for the case of nitrogen gas), any pressure near the minimum value would have yielded higher breakdown voltage.

***Allowable Subject Matter***

3. Claims 24 and 26 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten, to overcome the objections cited above, and in independent form including all of the limitations of the base claim and any intervening claims.

4. The following is a statement of reasons for the indication of allowable subject matter:

Regarding claims 24 and 26, the cited prior art of record taken alone or in combination fail to disclose as suggested a semiconductor device manufacturing method using a plasma processor, or a plasma processor, wherein charging of the component in the vacuum processing chamber is suppressed, In combination of the limitations of claims 1 and 12.

***Response to Arguments***

5. Applicant's arguments, filed 1/3/2008, with respect to the rejection(s) of the pending claim(s) have been fully considered and are persuasive in view of the new amendments to the claims. Therefore, the rejection has been withdrawn. However, upon further consideration, a new ground(s) of rejection is made in view of Ohmi et al. (US 6,217,633).

Any inquiry concerning this communication or earlier communications from the examiner should be directed to MAHMOUD DAHIMENE whose telephone number is

Art Unit: 1792

(571)272-2410. The examiner can normally be reached on week days from 8:00 AM. to 5:00 PM..

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Nadine Norton can be reached on (571) 272-1465. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

/Duy-Vu N Deo/  
Primary Examiner, Art Unit 1792

/M. D./  
Examiner, Art Unit 1792